

3 Integrated CMOS imaging array and dark current monitor

4 **ABSTRACT OF THE INVENTION**

5 A method, apparatus, and system for accounting for dark current in the output of an imaging  
6 array is presented. A dark current monitor on the monolithic semiconductor imaging array is  
7 provided. The dark current monitor may be darkened pixels of the imaging array, darkened  
8 pixels of another array, or a temperature monitor and associated circuitry necessary to calculate  
9 relative dark current from the monitored temperature.